

DOCKETED

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On the Rulemaking on New Energy Efficiency Standards

Additional submitted attachment is included below.

May 19, 2016

Commissioner Andrew McAllister
California Energy Commission
1516 Ninth Street
Sacramento, CA 95814
RE: Docket No. 14-AAER-2 Computers, Monitors, and Signage Displays Efficiency Standards

Dear Commissioner McAllister:

Power Integrations (PI) thanks you for the opportunity to provide comments on this rulemaking on new energy efficiency standards for computers and monitors.

PI is a San Jose, California based semiconductor company that designs and supplies electronic components used in AC-DC power supplies. Our integrated circuits enable compact, highly energy-efficient AC-DC power supplies used in a wide range of electronic products including mobile devices, TVs, PCs, appliances, smart utility meters and LED lights. Last year the company had revenues of \$343M from selling over 1 billion power conversion devices.

PI was asked by the NRDC and Aggios to design an efficient power supply for a demonstration of a computer system that would comply with the proposed CEC standard. The power supply goals were 300 W output power, good efficiency at both high power and low power idle modes, able to fit into an industry standard ATX package, utilization of standard components, and little or no Bill Of Material (BOM) cost penalty compared to currently available 300 W power supplies.

The power supply used in the Aggios demonstration uses a two converter approach, one for low load and one for high load. The supply automatically switches between the two circuits depending on the computer load requirements. It delivers 300 W (max) output, is 80+ silver compliant, has a Power Factor greater than 0.98, and is greater than 80% efficient at an idle load around 10 W. This design uses standard, in-production, off-the-shelf components. It fits into an ATX box. It could be scalable to higher power. We estimate the BOM cost for this design to be within the range of similarly rated power supplies.

In Power Integrations' opinion, available power supply technology or resulting incremental power supply cost should not be barriers when designing a computer required to be energy efficient during low power idle modes.

Sincerely,



Richard Fassler
Manager, Energy Efficiency Programs
Power Integrations